

UNIVERSITY OF MUMBAI
SCHEME OF INSTRUCTIONS & EXAMINATION

at

B.E.(Electronics Engineering)

(Revised from year -2001)

(Revised Scheme Considering 60 Minutes' Period Instead of 45 Minutes' Period as per AICTE Guide-lines)

SEMESTER VII

Sr. No.	Subjects	No. of Periods per Week			Duration of Theory Paper (Hrs)	Marks				
		Lectures	Practicals	Tutorials		Theory Paper	Term Work	Practical	Oral	Total
1.	Basics of VLSI (BVLSI)	4	2	-	3	100	25	-	25	150
2.	Instrumentation Systems (IS)	3	3	-	3	100	25	-	-	125
3.	Digital Communication (DCOM)	3	2	-	3	100	25	-	25	150
4.	Filter Theory and Applications (FTA)	4	2	-	3	100	25	-	-	125
5.	Elective –I	4	2	-	3	100	25	-	25	150
6.	Project –A	-	-	4	-	-	25	-	-	25
Total		18	10	4	-	500	150	-	75	725

Elective I
Image Processing (IP)
Process Control & Instrumentation (PCI)
Microcomputer System Design (MSD)
DSP Architectures and Systems(DSPAS)
Wireless Communication (WC)